

EXRESS MAIL MAILING LABEL NO. EL521272878US

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9P 2811

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Brent Keeth

Pierre C. Fazan

Serial No.: 08/530,661

Filed: 09/20/1995

For: SEMICONDUCTOR MEMORY  
CIRCUITRY

Group Art Unit: 2814

Examiner: Wille, Douglas A.

Atty. Dkt. No.: 11762.0266.NPUS00

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Please amend the claims of the above-identified application as follows. Attached as Appendix I is a version with markings to show changes made.

6. (Twice Amended) A semiconductor device including a memory, the semiconductor device comprising:

a semiconductor die encapsulated in a package, the package having an encapsulating body and electrically conductive interconnect pins extending outwardly from the body;

a total of from 16,000,000 to 17,000,000 functional and operably addressable memory cells arranged in multiple memory arrays formed on the die, the individual functional and operably addressable memory cells occupying area on the die within the memory arrays, the occupied area of all functional and addressable memory cells on the die having a total combined area which is no greater than  $14 \text{ mm}^2$ ; and